Appl. No. 10/751,170
Amdt. dated July 12, 2006
Reply to Office Action of January 18, 2006

PATENT

Amendments to the Specification:

Please replace the paragraph on page 6, lines 22 through lines 24, in the specification as filed with the following amended paragraph:

> In step 314, in the case where more than one SDRAM burst is required, the ~~arbiter 100~~ control logic 54 also determines SDRAM addresses for the remaining SDRAM bursts.